

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/559,908
Filing Date April 26, 2000
Inventor Zhiping Yin et al.
Assignee Micron Technology, Inc.
Group Art Unit 2815
Examiner G. Eckert II
Attorney's Docket No. MI22-1427
Title: Circuitry and Gate Stacks

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed after the filing of the Request for Continued Examination (RCE) Application and before receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

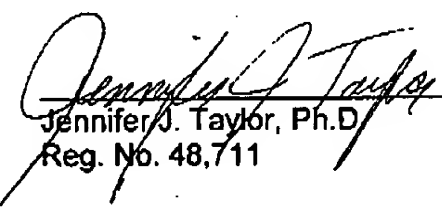
Respectfully submitted,

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Date: May 1, 2003


Jennifer J. Taylor, Ph.D.
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<h1>TRANSMITTAL FORM</h1> <p>(to be used for all correspondence after initial filing)</p>	Application Number	09/559,903
	Filing Date	April 26, 2000
	First Named Inventor	Zhiping Yin
	Group Art Unit	2815
	Examiner Name	G. Eckert II
Total Number of Pages in This Submission	Attorney Docket Number MI22-1427	

ENCLOSURES (check all that apply)		
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Firm or Individual name	Jennifer J. Taylor, Ph.D.; Wells St. John P.S.
Signature	<i>Jennifer J. Taylor</i>
Date	<i>May 1, 2003</i>

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